

Chapter 3 SOLUTIONS

1) a) $I_D = \frac{V_{in} - 2V_{D0n}}{R_1 + R_2}$
 $= \frac{2.5 - 1.4}{4k\Omega} = \frac{1.1V}{4k\Omega}$

$I_D = 275\mu A$

b) $I_S = 10^{-16} A$, $T = 300K$, $V_{D0n} = 0.7V$

$I_D = I_S (e^{V_D/\phi_T} - 1)$

where $\phi_T = \frac{kT}{q} = 26mV @ 300K$

★ $I_D = \frac{V_{in} - 2V_D}{R_1 + R_2} = I_S (e^{V_D/\phi_T} - 1)$
 $\frac{2.5 - 2V_D}{4k\Omega} = 10^{-16} (e^{V_D/0.026} - 1)$

iterating on this expression
 we can obtain.

$V_D = 0.628V$
 $I_D = 311\mu A$

c) SPICE

exercise 3.1

vin in 0 dc 2.5
 r1 in 2 2k
 d1 2 3 D
 r2 3 out 2k
 d2 out 0 D

.options post=2
 .op

.model D D level=1 is=1e-16 nr=0.5
 .end

 NODE VOLTAGE
 2 1.8750E+00
 3 1.2500E+00
 OUT 6.2501E-01

CURRENT 3.1249E-04

d) $I_S = 10^{-16} A$, $T = 300K$;
 again use ★ from b and iterate
 to find:

$V_D = 0.743V$
 $I_D = 253\mu A$

$I_S = 10^{-14} A$, $T = 350K$;

$V_D = 0.728V$
 $I_D = 261\mu A$

exercise 3.1d
 vin in 0 dc 2.5
 r1 in 2 2k
 d1 2 3 D
 r2 3 out 2k
 d2 out 0 D

.options post=2
 .op

* temp=27
 .temp 77
 * model D D level=1 is=1e-16 nr=0.5
 .model D D level=1 is=1e-14 nr=0.5
 .end

 t=27C
 NODE VOLTAGE
 2 1.9889E+00
 3 1.2500E+00
 IN 2.5000E+00
 OUT 7.3892E-01

ID 2.5554E-04

 t=77C
 NODE VOLTAGE
 2 1.7844E+00
 3 1.2500E+00
 IN 2.5000E+00
 OUT 5.3436E-01
 ID 3.3782E-04

Solution Manual For Jan Rabaey

Wayne Hendrix Wolf



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Power Aware Design Methodologies Massoud Pedram, Jan M. Rabaey, 2007-05-08 Power Aware Design Methodologies was conceived as an effort to bring all aspects of power aware design methodologies together in a single document. It covers several layers of the design hierarchy from technology circuit logic and architectural levels up to the system layer. It includes discussion of techniques and methodologies for improving the power efficiency of CMOS circuits digital and analog systems on chip microelectronic systems wirelessly networked systems of computational nodes and so on. In addition to providing an in depth analysis of the sources of power dissipation in VLSI circuits and systems and the technology and design trends this book provides a myriad of state of the art approaches to power optimization and control. The different chapters of Power Aware Design Methodologies have been written by leading researchers and experts in their respective areas. Contributions are from both academia and industry. The contributors have reported the various technologies methodologies and techniques in such a way that they are understandable and useful.

Building Embedded Systems Changyi Gu, 2016-05-26 Develop the software and hardware you never think about. We're talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now. Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level. Because yes many people quietly make a successful career doing just that. Building embedded systems can be both fun and intimidating. Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular. Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project. Programmers who know software will learn what they need to know about hardware. Engineers with hardware knowledge likewise will learn about the software side. Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices. Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems. He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field. His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project.

What You Will Learn

- Program embedded systems at the hardware level
- Learn current industry practices in firmware development
- Develop practical knowledge of embedded hardware options
- Create tight integration between software and hardware
- Practice a work flow leading to successful outcomes
- Build from transistor level to the system level
- Make sound choices between performance and cost

Who This Book Is For

Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware. Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book. Students in both

Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

VLSI Signal Processing, VII Jan Rabaey, Paul M. Chau, John Eldon, 1994 Low Power Design Essentials Jan

Rabaey, 2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer. The book lays the foundation with background chapters entitled Advanced MOS Transistors and Their Models and Power Basics. These chapters are followed by chapters on the design process including optimization, architecture, and algorithm level memory, run time, standby logic, and standby memory. Chapters on special topics are also included: power management and modal design, ultra low power, and low power design methodology and flows. The book concludes with a chapter on case studies as well as a chapter on Projection into the Future. These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide. All chapters have been drawn up specifically for self study. They aim, however, at different levels of understanding. All the chapters start with elementary material but most also contain advanced material.

Application Specific Processors for Numerical Algorithms Lars Erik Thon, 1992

Computers as

Components Marilyn Wolf, 2012-06-12 Computers as Components: Principles of Embedded Computing System Design, Third Edition, presents essential knowledge on embedded systems technology and techniques. Updated for today's embedded systems design methods, this volume features new examples including digital signal processing, multimedia, and cyber physical systems. It also covers the latest processors from Texas Instruments, ARM, and Microchip Technology, plus software, operating systems, networks, consumer devices, and more. Like the previous editions, this textbook uses real processors to demonstrate both technology and techniques, shows readers how to apply principles to actual design practice, stresses necessary fundamentals that can be applied to evolving technologies, and helps readers gain facility to design large, complex embedded systems. Updates in this edition include: description of cyber physical systems, exploration of the PIC and TI OMAP processors, high level representations of systems using signal flow graphs, enhanced material on interprocess communication and buffering in operating systems, and design examples that include an audio player, digital camera, and cell phone. The author maintains a robust ancillary site at <http://www.marilynwolf.us/CaC3e/index.html> which includes a variety of support materials for instructors and students, including PowerPoint slides for each chapter, lab assignments developed for multiple systems including the ARM based BeagleBoard, computer downloadable exercises, solutions, and source code, and links to resources and additional information on hardware, software, systems, and more. This book will appeal to students in an embedded systems design course as well as to researchers and savvy professionals schooled in hardware or software design. Description of cyber physical systems, physical systems with integrated computation to give new capabilities, exploration of the PIC and TI OMAP multiprocessors, high level representations of systems using signal flow graphs, enhanced material on interprocess communication and buffering in operating systems. Design examples include an audio player, digital camera, cell phone, and more.

Computers as Components Wayne Hendrix Wolf, 2005 This work unravels the complexity of embedded

systems e g cell phones microwaves and information appliances and of the process tools and techniques necessary for designing them

FinFETs and Other Multi-Gate Transistors J.-P. Colinge, 2008 This book explains the physics and properties of multi gate field effect transistors MuGFETs how they are made and how circuit designers can use them to improve the performances of integrated circuits It covers the emergence of quantum effects due to the reduced size of the devices and describes the evolution of the MOS transistor from classical structures to SOI silicon on insulator and then to MuGFETs

Adaptive Techniques for Dynamic Processor Optimization Alice Wang, Samuel Naffziger, 2008-07-23 The integrated circuit has evolved tremendously in recent years as Moore s Law has enabled exponentially more devices and functionality to be packed onto a single piece of silicon In some ways however these highly integrated circuits of which microprocessors are the flagship example have become victims of their own success Despite dramatic reductions in the switching energy of the transistors these reductions have kept pace neither with the increased integration levels nor with the higher switching frequencies In addition the atomic dimensions being utilized by these highly integrated processors have given rise to much higher levels of random and systematic variation which undercut the gains from process scaling that would otherwise be realized So these factors the increasing impact of variation and the struggle to control power consumption have given rise to a tremendous amount of innovation in the area of adaptive techniques for dynamic processor optimization The fundamental premise behind adaptive processor design is the recognition that variations in manufacturing and environment cause a statically configured operating point to be far too inefficient Inefficient designs waste power and performance and will quickly be surpassed by more adaptive designs just as it happens in the biological realm Organisms must adapt to survive and a similar trend is seen with processors those that are enabled to adapt to their environment will be far more competitive

mm-Wave Silicon Technology Ali M. Niknejad, Hossein Hashemi, 2008-01-03 This book compiles and presents the research results from the past five years in mm wave Silicon circuits This area has received a great deal of interest from the research community including several university and research groups The book covers device modeling circuit building blocks phased array systems and antennas and packaging It focuses on the techniques that uniquely take advantage of the scale and integration offered by silicon based technologies

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