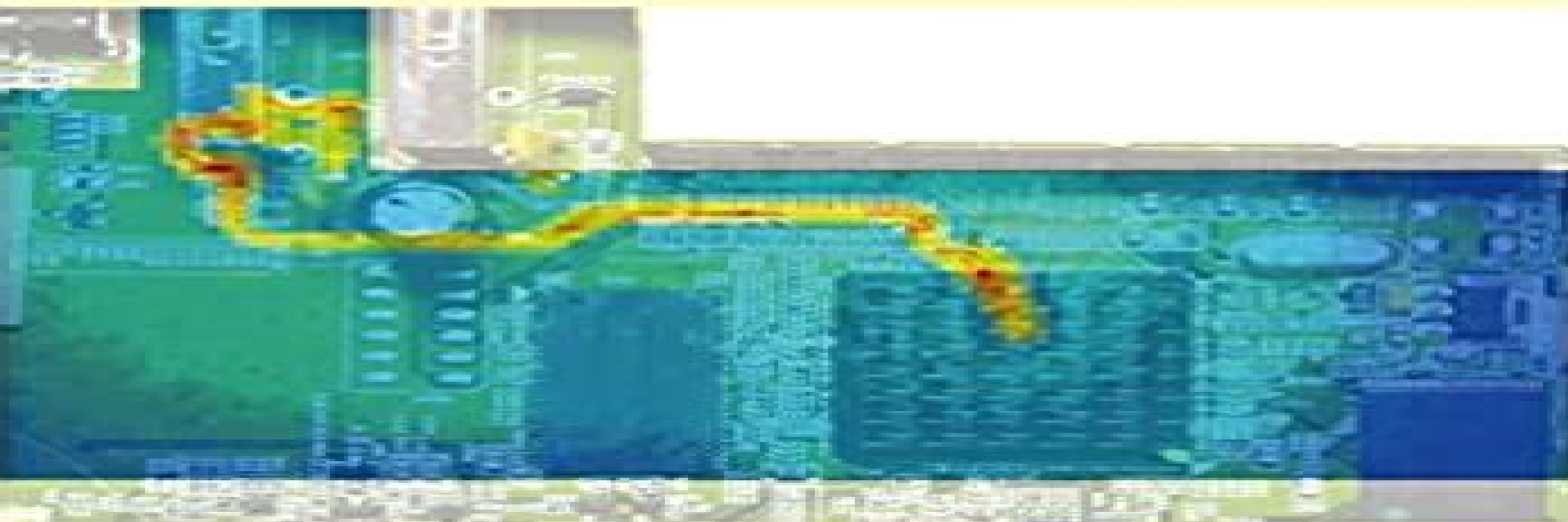


SYSTEM LEVEL ESD CO-DESIGN



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System Level ESD Co-Design Charvaka Duvvury, Harald Gossner, 2017-05-05 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerence Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs Key features Clarifies the concept of system level ESD protection Introduces a co design approach for ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design [Electromagnetic Compatibility for Space Systems](#)

[Design](#) Nikolopoulos, Christos D., 2018-03-02 In the aerospace industry avoiding operating issues especially in regard to space missions and satellite structures is crucial The vast majority of these issues can be traced to disturbances in the electromagnetic fields used Electromagnetic Compatibility for Space Systems Design is a critical scholarly resource that examines the applications of electromagnetic compatibility and electromagnetic interference in the space industry Featuring coverage on a wide range of topics such as magnetometers electromagnetic environmental effects and electromagnetic shielding this book is geared toward managers engineers and researchers seeking current research on the applications of electromagnetic technologies in the aerospace field **The ESD Control Program Handbook** Jeremy M.

Smallwood, 2020-11-09 Provides the understanding and practical skills needed to develop and maintain an effective ESD control program for manufacturing storage and handling of ESD sensitive components This essential guide to ESD control programs explains the principles and practice of ESD control in an easily accessible way whilst also providing more depth and a wealth of references for those who want to gain a deeper knowledge of the subject It describes static electricity and

ESD principles such as triboelectrification electrostatic fields and induced voltages with the minimum of theory or mathematics It is designed for the reader to dip into as required rather than need to read cover to cover The ESD Control Program Handbook begins with definitions and commonly used terminology followed by the principles of static electricity and ESD control Chapter 3 discusses ESD susceptible electronic devices and how ESD susceptibility of a component is measured This is followed by the Seven habits of a highly effective ESD program explaining the essential activities of an effective ESD control program While most texts mainly address manual handling of ESD susceptible devices Chapter 5 extends the discussion to ESD control in automated systems processes and handling which form a major part of modern electronic manufacture Chapter 6 deals with requirements for compliance given by the IEC 61340 5 1 and ANSI ESD S20 20 ESD control standards Chapter 7 gives an overview of the selection use care and maintenance of equipment and furniture commonly used to control ESD risks The chapter explains how these often work together as part of a system and must be specified with that in mind ESD protective packaging is available in an extraordinary range of forms from bags boxes and bubble wrap to tape and reel packaging for automated processes The principles and practice of this widely misunderstood area of ESD control are introduced in Chapter 8 The thorny question of how to evaluate an ESD control program is addressed in Chapter 9 with a goal of compliance with a standard as well as effective control of ESD risks and possible customer perceptions Whilst evaluating an existing ESD control program provides challenges developing an ESD control program from scratch provides others Chapter 10 gives an approach to this Standard test methods used in compliance with ESD control standards are explained and simple test procedures given in Chapter 11 ESD Training has long been recognised as essential in maintaining effective ESD control Chapter 12 discusses ways of covering essential topics and how to demonstrate static electricity in action The book ends with a look at where ESD control may go in the near future The ESD Control Program Handbook Gives readers a sound understanding of the subject to analyze the ESD control requirements of manufacturing processes and develop an effective ESD control program Provides practical knowledge as well as sufficient theory and background to understand the principles of ESD control Teaches how to track and identify how ESD risks arise and how to identify fitting means for minimizing or eliminating them Emphasizes working with modern ESD control program standards IEC 61340 5 1 and ESD S20 20 The ESD Control Program Handbook is an invaluable reference for anyone tasked with setting up evaluating or maintaining an effective ESD control program training personnel or making ESD control related measurements It would form an excellent basis for a University course on the subject as well as a guide and resource for industry professionals

Recent Trends on Electromagnetic Environmental Effects for Aeronautics and Space Applications Nikolopoulos, Christos D.,2020-11-27 Electromagnetic compatibility and regulatory compliance issues are subjects of great importance in electronics engineering Avoiding problems regarding an electronic system s operation while always important is especially critical in space missions and satellite structures Many problems can be traced to EM field

disturbances as interference from unintended sources and other electromagnetic phenomena As a result stringent requirements are to be met in terms of electromagnetic emissions levels The inclusion of this electromagnetic environment in the design of a multimillion mission can lead to a system that is able to withstand whatever challenge the environment throws at it Failure to do so may lead to important data corruption or loss destruction of expensive instruments waste of resources and even a total mission failure Research in this area focuses on the studying of the applications of electromagnetic compatibility and electromagnetic interference in the space industry Recent Trends on Electromagnetic Environmental Effects for Aeronautics and Space Applications will provide relevant theoretical frameworks and the latest empirical research findings in electromagnetic compatibility and electromagnetic interference EMC EMI for the aerospace industry This book examines all the necessary information for all matters that can possibly affect the system design of a spacecraft and can be a useful reference to space system engineers and more While highlighting topics such as artificial intelligence electromagnetic testing environmental shielding and EMC modelling techniques this book is ideal for professionals spacecraft designers science and data processing managers electrical and mechanical engineers EMC testing engineers and researchers working in the aerospace industry along with practitioners researchers academicians and students looking for necessary information for all the matters that can possibly affect the system design of a spacecraft

System Level ESD Co-design, 2015 Demystifies the concept of system level ESD and details its difference from the conventional component level ESD design and testing Describes the protection elements and designs and focuses on the co design an optimization methodology to address both issues in the same design space **System Level ESD Protection** Vladislav Vashchenko, Mirko Scholz, 2014-03-21 This book addresses key aspects of analog integrated circuits and systems design related to system level electrostatic discharge ESD protection It is an invaluable reference for anyone developing systems on chip SoC and systems on package SoP integrated with system level ESD protection The book focuses on both the design of semiconductor integrated circuit IC components with embedded on chip system level protection and IC system co design The readers will be enabled to bring the system level ESD protection solutions to the level of integrated circuits thereby reducing or completely eliminating the need for additional discrete components on the printed circuit board PCB and meeting system level ESD requirements The authors take a systematic approach based on IC system ESD protection co design A detailed description of the available IC level ESD testing methods is provided together with a discussion of the correlation between IC level and system level ESD testing methods The IC level ESD protection design is demonstrated with representative case studies which are analyzed with various numerical simulations and ESD testing The overall methodology for IC system ESD co design is presented as a step by step procedure that involves both ESD testing and numerical simulations **System Level ESD Co-Design** Charvaka Duvvury, Harald Gossner, 2015-09-08 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This

pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerence Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs Key features Clarifies the concept of system level ESD protection Introduces a co design approach for ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design

ESD Steven H. Voldman, 2015-06-22

ESD Circuits and Devices 2nd Edition provides a clear picture of layout and design of digital analog radio frequency RF and power applications for protection from electrostatic discharge ESD electrical overstress EOS and latchup phenomena from a generalist perspective and design synthesis practices providing optimum solutions in advanced technologies New features in the 2nd edition Expanded treatment of ESD and analog design of passive devices of resistors capacitors inductors and active devices of diodes bipolar junction transistors MOSFETs and FINFETs Increased focus on ESD power clamps for power rails for CMOS Bipolar and BiCMOS Co synthesizing of semiconductor chip architecture and floor planning with ESD design practices for analog and mixed signal applications Illustrates the influence of analog design practices on ESD design circuitry from integration synthesis and layout to symmetry matching inter digitation and common centroid techniques Increased emphasis on system level testing conforming to IEC 61000 4 2 and IEC 61000 4 5 Improved coverage of low capacitance ESD scaling of devices and oxide scaling challenges

ESD Circuits and Devices 2nd Edition is an essential reference to ESD circuit semiconductor engineers and quality reliability analysis engineers It is also useful for graduate and undergraduate students in electrical engineering semiconductor sciences microelectronics and IC design

ESD Testing Steven H. Voldman, 2016-12-19 With the evolution of semiconductor technology and global diversification of the semiconductor business testing of semiconductor devices to systems for electrostatic discharge ESD and electrical overstress EOS has

increased in importance ESD Testing From Components to Systems updates the reader in the new tests test models and techniques in the characterization of semiconductor components for ESD EOS and latchup Key features Provides understanding and knowledge of ESD models and specifications including human body model HBM machine model MM charged device model CDM charged board model CBM cable discharge events CDE human metal model HMM IEC 61000 4 2 and IEC 61000 4 5 Discusses new testing methodologies such as transmission line pulse TLP to very fast transmission line pulse VF TLP and future methods of long pulse TLP to ultra fast TLP UF TLP Describes both conventional testing and new testing techniques for both chip and system level evaluation Addresses EOS testing electromagnetic compatibility EMC scanning to current reconstruction methods Discusses latchup characterization and testing methodologies for evaluation of semiconductor technology to product testing ESD Testing From Components to Systems is part of the authors series of books on electrostatic discharge ESD protection this book will be an invaluable reference for the professional semiconductor chip and system level ESD and EOS test engineer Semiconductor device and process development circuit designers quality reliability and failure analysis engineers will also find it an essential reference In addition its academic treatment will appeal to both senior and graduate students with interests in semiconductor process device physics semiconductor testing and experimental work

The ESD Handbook Steven H. Voldman, 2021-04-12 A practical and comprehensive reference that explores Electrostatic Discharge ESD in semiconductor components and electronic systems The ESD Handbook offers a comprehensive reference that explores topics relevant to ESD design in semiconductor components and explores ESD in various systems Electrostatic discharge is a common problem in the semiconductor environment and this reference fills a gap in the literature by discussing ESD protection Written by a noted expert on the topic the text offers a topic by topic reference that includes illustrative figures discussions and drawings The handbook covers a wide range of topics including ESD in manufacturing garments wrist straps and shoes ESD Testing ESD device physics ESD semiconductor process effects ESD failure mechanisms ESD circuits in different technologies CMOS Bipolar etc ESD circuit types Pin Power Pin to Pin etc and much more In addition the text includes a glossary index tables illustrations and a variety of case studies Contains a well organized reference that provides a quick review on a range of ESD topics Fills the gap in the current literature by providing information from purely scientific and physical aspects to practical applications Offers information in clear and accessible terms Written by the accomplished author of the popular ESD book series Written for technicians operators engineers circuit designers and failure analysis engineers The ESD Handbook contains an accessible reference to ESD design and ESD systems

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