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Synthesis and Optimization of FPGA-Based Systems

Synthesis And Optimization Of Fpga Based Systems

Lecture Notes In Electrical Engineering

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Synthesis And Optimization Of Fpga Based Systems Lecture Notes In Electrical Engineering:

Synthesis and Optimization of FPGA-Based Systems Valery Sklyarov, Ioulia Skliarova, Alexander Barkalov, Larysa Titarenko, 2014-03-14 The book is composed of two parts The first part introduces the concepts of the design of digital systems using contemporary field programmable gate arrays FPGAs Various design techniques are discussed and illustrated by examples The operation and effectiveness of these techniques is demonstrated through experiments that use relatively cheap prototyping boards that are widely available The book begins with easily understandable introductory sections continues with commonly used digital circuits and then gradually extends to more advanced topics The advanced topics include novel techniques where parallelism is applied extensively These techniques involve not only core reconfigurable logical elements but also use embedded blocks such as memories and digital signal processing slices and interactions with general purpose and application specific computing systems Fully synthesizable specifications are provided in a hardware description language VHDL and are ready to be tested and incorporated in engineering designs A number of practical applications are discussed from areas such as data processing and vector based computations e g Hamming weight counters comparators The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources minimizing delays and achieving greater optimization of circuits and systems

Logic Synthesis for FPGA-Based Mealy Finite State Machines Alexander Barkalov, Larysa Titarenko, Kazimierz Krzywicki, 2024-12-04 This book is devoted to the logic synthesis of field programmable gate array FPGA based circuits of Mealy finite state machines FSM Three new methods of state assignment are proposed which allows obtaining FSM circuits required minimum amount of internal chip resources Logic Synthesis for FPGA Based Mealy Finite State Machines Structural Decomposition in Logic Design contains several original synthesis and optimization methods based on the structural decomposition of FPGA based FSM circuits developed by the authors To optimize FSM circuits the authors introduce the use of three methods of state assignment twofold extended and composite These methods allow for the creation of two or three level architectures of FSM circuits The authors also demonstrate how the proposed methods FSM architectures and synthesis methods can replace known solutions based on either functional decomposition or classical methods of structural decomposition The authors also show how these architectures have regular systems of interconnections and demonstrate positive features compared to methods based on functional decomposition including producing circuits with fewer elements that are faster and consume less power than their counterparts The book includes experimental results proving the efficiency of the proposed solutions and compares the numbers in Look up Tables LUTs showing the performance maximum operating frequency and power consumption for various methods of state assignment The audience for this book is students researchers and engineers specializing in computer science engineering electronics and telecommunications It will be especially useful for engineers working within the scope of algorithms hardware based software accelerators and control

units and systems based on the use of FPGAs Logic Synthesis for FPGA-Based Finite State Machines Alexander Barkalov, Larysa Titarenko, Malgorzata Kolopienczyk, Kamil Mielcarek, Grzegorz Bazydło, 2015-10-15 This book discusses control units represented by the model of a finite state machine FSM It contains various original methods and takes into account the peculiarities of field programmable gate arrays FPGA chips and a FSM model It shows that one of the peculiarities of FPGA chips is the existence of embedded memory blocks EMB The book is devoted to the solution of problems of logic synthesis and reduction of hardware amount in control units The book will be interesting and useful for researchers and PhD students in the area of Electrical Engineering and Computer Science as well as for designers of modern digital systems *Logic Synthesis for FPGA-Based Control Units* Alexander Barkalov, Larysa Titarenko, Kamil Mielcarek, Sławomir Chmielewski, 2020-01-08 This book focuses on control units which are a vital part of modern digital systems and responsible for the efficiency of controlled systems The model of a finite state machine FSM is often used to represent the behavior of a control unit As a rule control units have irregular structures that make it impossible to design their logic circuits using the standard library cells Design methods depend strongly on such factors as the FSM used specific features of the logic elements implemented in the FSM logic circuit and the characteristics of the control algorithm to be interpreted This book discusses Moore and Mealy FSMs implemented with FPGA chips including look up table elements LUT and embedded memory blocks EMB It is crucial to minimize the number of LUTs and EMBs in an FSM logic circuit as well as to make the interconnections between the logic elements more regular and various methods of structural decompositions can be used to solve this problem These methods are reduced to the presentation of an FSM circuit as a composition of different logic blocks the majority of which implement systems of intermediate logic functions different and much simpler than input memory functions and FSM output functions The structural decomposition results in multilevel FSM circuits having fewer logic elements than equivalent single level circuits The book describes well known methods of structural decomposition and proposes new ones examining their impact on the final amount of hardware in an FSM circuit It is of interest to students and postgraduates in the area of Computer Science as well as experts involved in designing digital systems with complex control units The proposed models and design methods open new possibilities for creating logic circuits of control units with an optimal amount of hardware and regular interconnections *Logic Synthesis for VLSI-Based Combined Finite State Machines* Alexander Barkalov, Larysa Titarenko, Kamil Mielcarek, Małgorzata Mazurkiewicz, Elżbieta Kawecka, 2022-11-24 The book is devoted to design and optimization of control units represented by combined finite state machines CFSMs The CFSMs combine features of both Mealy and Moore FSMs Having states of Moore FSM they produce output signals of both Mealy and Moore types To optimize the circuits of CFSMs we propose to use optimization methods targeting both Mealy and Moore FSMs The book contains some original synthesis and optimization methods targeting hardware reduction in VLSI based CFSM circuits These methods take into account the peculiarities of both a CFSM model and a VLSI chip in use The

optimization is achieved due to combining classical optimization methods with new methods proposed in this book. These new methods are a mixed encoding of collections of microoperations and a twofold state assignment in CFSMs. All proposed methods target reducing the numbers of arguments in systems of Boolean functions representing CFSM circuits. Also we propose to use classes of pseudoequivalent states of Moore FSMs to reduce the number of product terms in these systems. The book includes a lot of examples which contributes to a better understanding of the features of the synthesis methods under consideration. This is the first book entirely devoted to the problems associated with synthesis and optimization of VLSI based CFSMs. We hope that the book will be interesting and useful for students and PhD students in the area of Computer Science as well as for designers of various digital systems. We think that proposed CFSM models enlarge the class of models applied for implementation of control units with modern VLSI chips.

Foundations of Embedded Systems Alexander Barkalov, Larysa Titarenko, Małgorzata Mazurkiewicz, 2019-02-04. This book is devoted to embedded systems (ESs) which can now be found in practically all fields of human activity. Embedded systems are essentially a special class of computing systems designed for monitoring and controlling objects of the physical world. The book begins by discussing the distinctive features of ESs above all their cybernetic physical character and how they can be designed to deliver the required performance with a minimum amount of hardware. In turn it presents a range of design methodologies. Considerable attention is paid to the hardware implementation of computational algorithms. It is shown that different parts of complex ESs could be implemented using models of finite state machines (FSMs). Also field programmable gate arrays (FPGAs) are very often used to implement different hardware accelerators in ESs. The book pays considerable attention to design methods for FPGA based FSMs. Before the closing section turns to programmable logic controllers widely used in industry. This book will be interesting and useful for students and postgraduates in the area of Computer Science as well as for designers of embedded systems. In addition it offers a good point of departure for creating embedded systems for various spheres of human activity.

Design of Reconfigurable Logic Controllers Andrei Karatkevich, Arkadiusz Bukowiec, Michał Doligalski, Jacek Tkacz, 2015-12-23. This book presents the original concepts and modern techniques for specification, synthesis, optimisation and implementation of parallel logical control devices. It deals with essential problems of reconfigurable control systems like dependability, modularity and portability. Reconfigurable systems require a wider variety of design and verification options than the application specific integrated circuits. The book presents a comprehensive selection of possible design techniques. The diversity of the modelling approaches covers Petri nets, state machines and activity diagrams. The preferences of the presented optimization and synthesis methods are not limited to increasing of the efficiency of resource use. One of the biggest advantages of the presented methods is the platform independence. The FPGA devices and single board computers are some of the examples of possible platforms. These issues and problems are illustrated with practical cases of complete control systems. If you expect a new look at the reconfigurable systems designing process or need ideas for improving the quality of

the project this book is a good choice g process or need ideas for improving the quality of the project this book is a good choice

Technology Mapping for LUT-Based FPGA Marcin Kubica,Adam Opara,Dariusz Kania,2020-11-07 This book covers selected topics of automated logic synthesis dedicated to FPGAs The authors focused on two main problems decomposition of the multioutput functions and technology mapping Additionally the idea of using binary decision diagrams BDD in these processes was presented The book is a scientific monograph summarizing the authors many years of research As a result it contains a large number of experimental results which makes it a valuable source for other researchers The book has a significant didactic value Its arrangement allows for a gradual transition from basic things e g description of logic functions to much more complex issues This approach allows less advanced readers to better understand the described problems In addition the authors made sure that the issues described in the book were supported by practical examples thanks to which the reader can independently analyze even the most complex problems described in the book

Technological Innovation for Collective Awareness Systems Luis M. Camarinha-Matos,Nuno S. Barrento,Ricardo Mendonça,2014-04-02 This book constitutes the refereed proceedings of the 5th IFIP WG 5.5 SOCOLNET Doctoral Conference on Computing Electrical and Industrial Systems DoCEIS 2014 held in Costa de Caparica Portugal in April 2014 The 68 revised full papers were carefully reviewed and selected from numerous submissions They cover a wide spectrum of topics ranging from collaborative enterprise networks to microelectronics The papers are organized in the following topical sections collaborative networks computational systems self organizing manufacturing systems monitoring and supervision systems advances in manufacturing human computer interfaces robotics and mechatronics Petri nets multi energy systems monitoring and control in energy modelling and simulation in energy optimization issues in energy operation issues in energy power conversion telecommunications electronics design electronics RF applications and electronics devices

Biomedical Signals Based Computer-Aided Diagnosis for Neurological Disorders M. Murugappan,Yuvaraj Rajamanickam,2022-06-17 Biomedical signals provide unprecedented insight into abnormal or anomalous neurological conditions The computer aided diagnosis CAD system plays a key role in detecting neurological abnormalities and improving diagnosis and treatment consistency in medicine This book covers different aspects of biomedical signals based systems used in the automatic detection identification of neurological disorders Several biomedical signals are introduced and analyzed including electroencephalogram EEG electrocardiogram ECG heart rate HR magnetoencephalogram MEG and electromyogram EMG It explains the role of the CAD system in processing biomedical signals and the application to neurological disorder diagnosis The book provides the basics of biomedical signal processing optimization methods and machine learning deep learning techniques used in designing CAD systems for neurological disorders

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