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SYSTEMATIC METHODS FOR DESIGN OF VLSI SIGNAL PROCESSING
ARRAYS FOR COMMUNICATIONS APPLICATIONS

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Thomas Kailath
Information Systems Laboratory
Department of Electrical Engineering
Stanford University
Stanford, CA 94305-4055

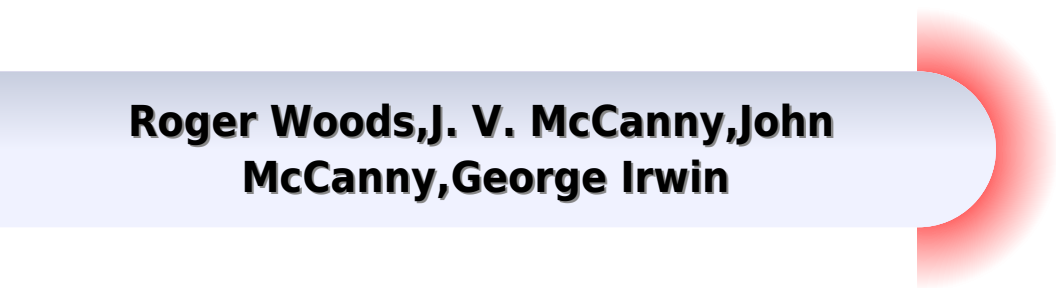
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Systematic Methods For Design Of Vlsi Signal Processing Arrays For Communications Applications

**Roger Woods, J. V. McCanny, John
McCanny, George Irwin**



Systematic Methods For Design Of Vlsi Signal Processing Arrays For Communications Applications:

Systematic Methods for Design of VLSI Signal Processing Arrays for Communications Applications, 1989 This is the final report on our work under ONR Contract N00D14 86 K 0726 August 1 1986 through July 31 1989 The major results are in two areas 1 Studies of systematic design procedures for a class of structured algorithms often encountered in signal processing applications These are what we have called Regular Iterative Algorithms RIAs for which our results are summarized in Section 2 2 The other major area of effort was the study of a notable family of algorithms that are not in the RIA form viz those associated with Viterbi decoding of convolutional and trellis codes or more generally with shortest path problems in graphs MM Parallel Processing on VLSI Arrays Josef A. Nossek, 2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publication on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical systems i e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PDEs It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems of PDEs involving time and finite propagation speed The basic ideas are explained using Maxwell's equations as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology**, 1998 **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya, Ed F. Deprettere, Rainer Leupers, Jarmo Takala, 2010-09-10 It gives me immense pleasure to introduce this timely handbook to the research development communities in the field of signal processing systems SPS This is the first of its kind and represents state of the arts coverage of research in this field The driving force behind information technologies IT hinges critically upon the major advances in both component integration and system integration The major breakthrough for the former is undoubtedly the invention of IC in the 50s by Jack S Kilby the Nobel Prize Laureate in Physics 2000 In an integrated circuit all components were made of the same semiconductor material Beginning with the pocket calculator in 1964 there have been many increasingly complex applications followed In fact processing gates and memory storage on a chip have since then grown at an exponential rate following Moore's Law Moore himself admitted that Moore's Law had turned out to be more

accurate longer lasting and deeper in impact than he ever imagined With greater device integration various signal processing systems have been realized for many killer IT applications Further breakthroughs in computer sciences and Internet technologies have also catalyzed large scale system integration All these have led to today's IT revolution which has profound impacts on our lifestyle and overall prospect of humanity It is hard to imagine life today without mobiles or Internets The success of SPS requires a well concerted integrated approach from multiple disciplines such as device design and application

Algorithms and Parallel VLSI Architectures III M. Moonen, F. Catthoor, 1995-03-16 A comprehensive overview of the current evolution of research in algorithms architectures and compilation for parallel systems is provided by this publication The contributions focus specifically on domains where embedded systems are required either oriented to application specific or to programmable realisations These are crucial in domains such as audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multimedia radar and sonar The book will be of particular interest to the academic community because of the detailed descriptions of research results presented In addition many contributions feature the real life applications that are responsible for driving research and the impact of their specific characteristics on the methodologies is assessed The publication will also be of considerable value to senior design engineers and CAD managers in the industrial arena who wish either to anticipate the evolution of commercially available design tools or to utilize the presented concepts in their own R D programmes

VLSI Systems for DSP and Control Roger Woods, J. V. McCanny, John McCanny, George Irwin, 1991

Signal Processing for Wireless Communication Systems H. Vincent Poor, Lang Tong, 2006-01-19 Signal Processing for Wireless Communication Systems brings together in one place important contributions and up to date research results in this fast moving area The Contributors to this work were selected from leading researchers and practitioners in this field The book's 18 chapters are divided into three areas systems Networks and Implementation Issues Channel Estimation and Equalization and Multiuser Detection The Work originally published as Volume 30 Numbers 1-3 of the Journal of VLSI Signal Processing Systems for Signal Image and Video Technology will be valuable to anyone working or researching in the field of wireless communication systems It serves as an excellent reference providing insight into some of the most challenging issues being examined today

Custom Memory Management Methodology Francky Catthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 2013-03-09 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989-1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red

thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large amounts of data

Low-Power CMOS Design
Anantha Chandrakasan, Robert W. Brodersen, 1998-02-11 This collection of important papers provides a comprehensive overview of low power system design from component technologies and circuits to architecture system design and CAD techniques LOW POWER CMOS DESIGN summarizes the key low power contributions through papers written by experts in this evolving field

Government Reports Announcements & Index, 1995

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