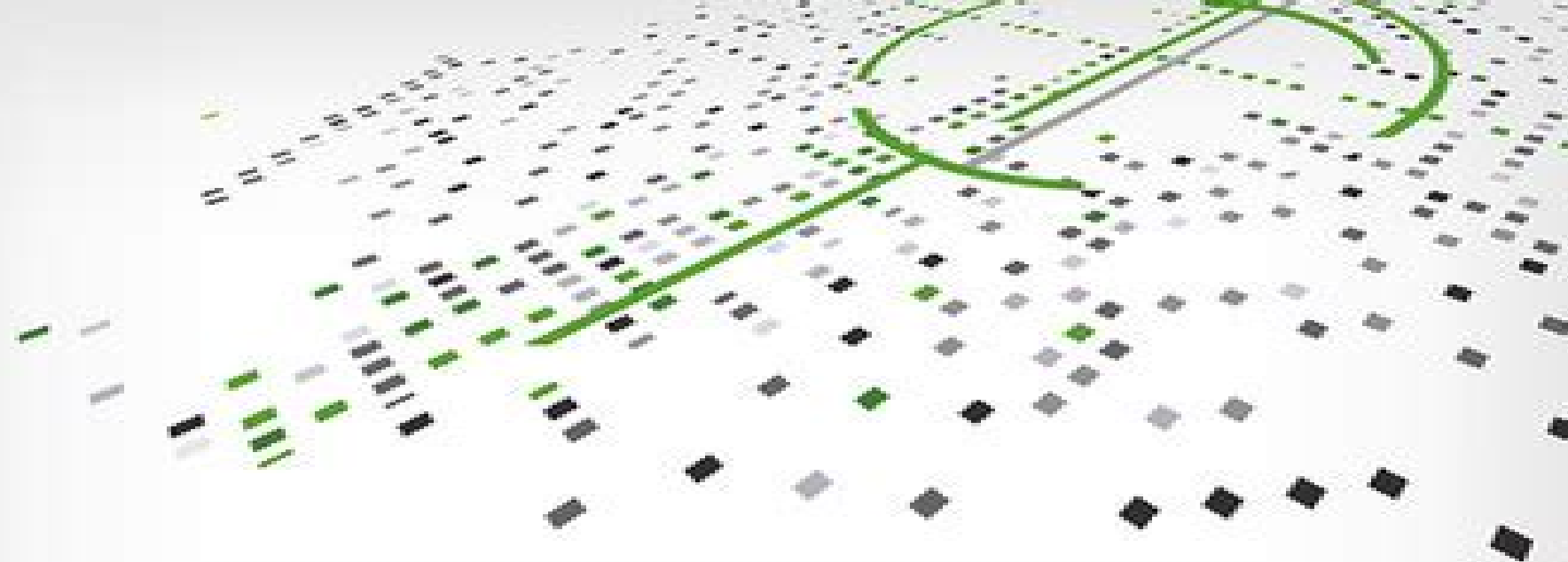




SYSTEM ON CHIP INTERFACES FOR LOW POWER DESIGN



SANJEEB MISHRA ■ NEERAJ KUMAR SINGH
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