

SMARTPHONE ENERGY CONSUMPTION



Sasu Tarkoma
Matti Siekkinen
Eemil Lagerspetz
Yu Xiao

Smartphone Energy Consumption Modeling And Optimization

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Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Vassilis Paliouras,2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation PATMOS 2005 held in Leuven Belgium in September 2005 The 74 revised full papers presented were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on low power processors code optimization for low power high level design telecommunications and signal processing low power circuits system on chip design busses and interconnections modeling design automation low power techniques memory and register files applications digital circuits and analog and physical design

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applications with an emphasis on energy efficiency The future research directions are also highlighted in this book to enrich the global market place of mobile cloud computing services facilitating the scientific industrial business and consumer applications We expect that the book will serve as a reference to a large number of readers including researchers system architects practitioners and graduate level students This book focuses on an emerging area that has considerable research interest momentum and interest of commercial developers The target reader of this book are professional developers under graduate and post graduate students and researchers As mobile cloud computing as well as green computing will have a major impact on the quality of science and society over the next few years its knowledge will enrich our readers to be at the forefront of the field This book reports the latest research advances in the area of green mobile cloud computing The book covers the architecture services methods applications and future research directions of green mobile cloud computing

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