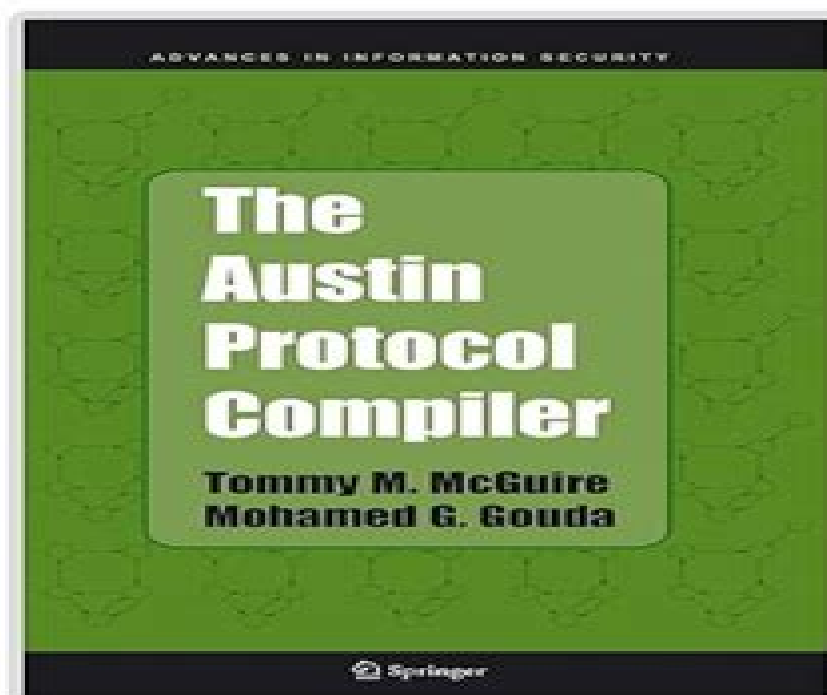


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The Austin Protocol Compiler The Austin Protocol Compiler

**Christopher Kruegel, Fredrik
Valeur, Giovanni Vigna**



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The Austin Protocol Compiler Tommy M. McGuire, Mohamed G. Gouda, 2006-01-20 There are two groups of researchers who are interested in designing network protocols and who cannot yet effectively communicate with one another concerning these protocols. The first is the group of protocol verifiers and the second is the group of protocol implementors. The main reason for the lack of effective communication between these two groups is that these groups use languages with quite different semantics to specify network protocols. On one hand, the protocol verifiers use specification languages whose semantics are abstract, coarse grained, and with large atomicity. Clearly, protocol specifications that are developed based on such semantics are easier to prove correct. On the other hand, the protocol implementors use specification languages whose semantics are concrete, fine grained, and with small atomicity. Protocol specifications that are developed based on such semantics are easier to implement using system programming languages such as C, C++, and Java. To help in closing this communication gap between the group of protocol verifiers and the group of protocol implementors, we present in this monograph a protocol specification language called the Timed Abstract Protocol or TAP for short notation. This notation is greatly influenced by the Abstract Protocol Notation in the textbook *Elements of Network Protocol Design* written by the second author, Mohamed G. Gouda. The TAP notation has two types of semantics: an abstract semantics that appeals to the protocol verifiers and a concrete semantics that appeals to the protocol implementors group. Elements of Compiler Design Alexander Meduna, 2007-12-03 Maintaining a balance between a theoretical and practical approach to this important subject, *Elements of Compiler Design* serves as an introduction to compiler writing for undergraduate students. From a theoretical viewpoint, it introduces rudimentary models such as automata and grammars that underlie compilation and its essential phases. Based on these models, the author details the concepts, methods, and techniques employed in compiler design in a clear and easy-to-follow way. From a practical point of view, the book describes how compilation techniques are implemented. In fact, throughout the text, a case study illustrates the design of a new programming language and the construction of its compiler. While discussing various compilation techniques, the author demonstrates their implementation through this case study. In addition, the book presents many detailed examples and computer programs to emphasize the applications of the compiler algorithms. After studying this self-contained textbook, students should understand the compilation process, be able to write a simple real compiler, and easily follow advanced books on the subject. **Self-Stabilizing Systems** Ted Herman, Sébastien Tixeuil, 2005-10-27 This book constitutes the refereed proceedings of the 7th International Symposium on Self-Stabilizing Systems (SSS 2005) held in Barcelona, Spain, in October 2005. The 15 revised full papers presented were carefully reviewed and selected from 33 submissions. The papers address classical topics of self-stabilization, prevailing extensions to the field such as snap stabilization, code stabilization, self-stabilization with either dynamic faulty or Byzantine components, or deal with applications of self-stabilization either related to operating systems security or mobile and ad hoc networks.

Handbook of Security and Networks Yang Xiao, Frank Haizhon Li, Hui Chen, 2011 This valuable handbook is a comprehensive compilation of state of art advances on security in computer networks More than 40 internationally recognized authorities in the field of security and networks contribute articles in their areas of expertise These international researchers and practitioners are from highly respected universities renowned research institutions and IT companies from all over the world Each self contained chapter covers one essential research topic on security in computer networks Through the efforts of all the authors all chapters are written in a uniformed style each containing which contains a comprehensive overview the latest pioneering work and future research direction of a research topic

Formal Languages and Computation Alexander Meduna, 2014-02-11 Formal Languages and Computation Models and Their Applications gives a clear comprehensive introduction to formal language theory and its applications in computer science It covers all rudimental topics concerning formal languages and their models especially grammars and automata and sketches the basic ideas underlying the theory of computatio

Understanding Intrusion Detection through Visualization Stefan Axelsson, David Sands, 2006-06-01 This monograph is the outgrowth of Stefan Axelsson s PhD Dissertation at Chalmers University in G teborg Sweden The dissertation in turn collects a number of research efforts performed over a period of six years or so into a coherent whole It was my honor to serve as the opponent at Dr Axelsson s examination In the Swedish system it is the job of the opponent to place the candidate s work into a broader perspective demonstrating its significance and contributions to the field and then to introduce the work to the attendees at the examination This done the candidate presents the technical details of the work and the opponent critiques the work giving the candidate the opportunity to defend it This forward is adapted from the introduction that I gave at the examination and should serve to acquaint the reader not only with the work at hand but also with the field to which it applies The title of the work Under standing Intrusion Detection Through Visualization is particularly telling As is the case with any good piece of research we hope to gain an understanding of a problem not just a recipe or simple solution of immediate but limited utility For much of its formative period computer security concentrated on devel oping systems that in effect embodied a fortress model of protection

CryptoGraphics Debra Cook, Angelos D. Keromytis, 2006-09-10 Software that covertly monitors user actions also known as spyware has become a first level security threat due to its ubiquity and the difficulty of detecting and removing it This is especially so for video conferencing thin client computing and Internet cafes *CryptoGraphics Exploiting Graphics Cards for Security* explores the potential for implementing ciphers within GPUs and describes the relevance of GPU based encryption to the security of applications involving remote displays As the processing power of GPUs increases research involving the use of GPUs for general purpose computing has arisen This work extends such research by considering the use of a GPU as a parallel processor for encrypting data The authors evaluate the operations found in symmetric and asymmetric key ciphers to determine if encryption can be programmed in existing GPUs A detailed description for a GPU based implementation of AES

is provided The feasibility of GPU based encryption allows the authors to explore the use of a GPU as a trusted system component Unencrypted display data can be confined to the GPU to avoid exposing it to any malware running on the operating system

Multivariate Public Key Cryptosystems Jintai Ding,Jason E. Gower,Dieter S. Schmidt,2006-11-24

Multivariate public key cryptosystems MPKC is a fast developing new area in cryptography In the past 10 years MPKC schemes have increasingly been seen as a possible alternative to number theoretic based cryptosystems such as RSA as they are generally more efficient in terms of computational effort As quantum computers are developed MPKC will become a necessary alternative Multivariate Public Key Cryptosystems systematically presents the subject matter for a broad audience Information security experts in industry can use the book as a guide for understanding what is needed to implement these cryptosystems for practical applications and researchers in both computer science and mathematics will find this book a good starting point for exploring this new field It is also suitable as a textbook for advanced level students Written more from a computational perspective the authors provide the necessary mathematical theory behind MPKC students with some previous exposure to abstract algebra will be well prepared to read and understand the material

Intrusion Detection and Correlation Christopher Kruegel,Fredrik Valeur,Giovanni Vigna,2005-12-29

Intrusion Detection and Correlation Challenges and Solutions presents intrusion detection systems IDSs and addresses the problem of managing and correlating the alerts produced This volume discusses the role of intrusion detection in the realm of network security with comparisons to traditional methods such as firewalls and cryptography The Internet is omnipresent and companies have increasingly put critical resources online This has given rise to the activities of cyber criminals Virtually all organizations face increasing threats to their networks and the services they provide Intrusion detection systems IDSs take increased pounding for failing to meet the expectations researchers and IDS vendors continually raise Promises that IDSs are capable of reliably identifying malicious activity in large networks were premature and never tuned into reality While virus scanners and firewalls have visible benefits and remain virtually unnoticed during normal operations the situation is different with intrusion detection sensors State of the art IDSs produce hundreds or even thousands of alerts every day Unfortunately almost all of these alerts are false positives that is they are not related to security relevant incidents Intrusion Detection and Correlation Challenges and Solutions analyzes the challenges in interpreting and combining i e correlating alerts produced by these systems In addition existing academic and commercial systems are classified their advantage and shortcomings are presented especially in the case of deployment in large real world sites

High Performance Embedded Architectures and Compilers Tom Conte,Nacho Navarro,Wen-mei W. Hwu,Mateo Valero,Theo Ungerer,2005-11-04

As Chairmen of HiPEAC 2005 we have the pleasure of welcoming you to the proceedings of the first international conference promoted by the HiPEAC Network of Excellence During the last year HiPEAC has been building its clusters of researchers in computer architecture and advanced compiler techniques for embedded and high performance computers Recently the Summer School has been the seed for a

fruitful collaboration of renowned international faculty and young researchers from 23 countries with fresh new ideas. Now the conference promises to be among the premier forums for discussion and debate on these research topics. The prestige of a symposium is mainly determined by the quality of its technical program. This first program lived up to our high expectations thanks to the large number of strong submissions. The Program Committee received a total of 84 submissions; only 17 were selected for presentation as full length papers and another one as an invited paper. Each paper was rigorously reviewed by three Program Committee members and at least one external referee. Many reviewers spent a great amount of effort to provide detailed feedback. In many cases such feedback along with constructive shepherding resulted in dramatic improvement in the quality of accepted papers. The names of the Program Committee members and the referees are listed in the proceedings. The net result of this team effort is that the symposium proceedings include outstanding contributions by authors from nine countries in three continents. In addition to paper presentations this first HiPEAC conference featured two keynotes delivered by prominent researchers from industry and academia.

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