

IMPLEMENTING EMBEDDED SYSTEMS HARDWARE/SOFTWARE CODESIGN

Concurrency in Embedded Systems

Concurrency refers to the way multiple tasks or processes run simultaneously on an embedded system.

Task Management Techniques:

In implementing embedded systems, task management techniques like prioritization, preemption, and synchronization are used to manage task concurrency effectively.

Prioritization:

Tasks can be prioritized based on their importance and urgency. This allows the system to allocate resources to the most important tasks first, ensuring that critical functions are executed in a timely manner.

Preemption:

Preemption is the act of temporarily interrupting a task in order to execute a higher priority task. This ensures that the higher priority task gets executed before the lower priority task resumes.

Synchronization:

Synchronization is the process of coordinating and managing the execution of multiple tasks in a way that ensures data consistency and avoids race conditions.

Software Engineering For Embedded Systems Chapter 2

Embedded Systems Hardwaresoftware Co Development

Robert Oshana



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Software Engineering for Embedded Systems Frank Schirrmeister, 2013-04-01 When planning the development of modern embedded systems hardware and software cannot be considered independently Over the last two decades chip and system complexity has seen an enormous amount of growth while more and more system functionality has moved from dedicated hardware implementation into software executing on general purposed embedded processors By 2010 the development effort for software had outgrown the development efforts for hardware and the complexity trend continues in favor of software Traditional design techniques such as independent hardware and software design are being challenged due to heterogeneous models and applications being integrated to create a complex system on chip Using proper techniques of hardware software codesign designers consider the trade offs in the way hardware and software components of a system work together to exhibit a specified behavior given a set of performance goals and technology This chapter will cover these topics

Software Engineering for Embedded Systems Robert Oshana, 2013-04-01 This Expert Guide gives you the techniques and technologies in software engineering to optimally design and implement your embedded system Written by experts with a solutions focus this encyclopedic reference gives you an indispensable aid to tackling the day to day problems when using software engineering methods to develop your embedded systems With this book you will learn The principles of good architecture for an embedded system Design practices to help make your embedded project successful Details on principles that are often a part of embedded systems including digital signal processing safety critical principles and development processes Techniques for setting up a performance engineering strategy for your embedded system software How to develop user interfaces for embedded systems Strategies for testing and deploying your embedded system and ensuring quality development processes Practical techniques for optimizing embedded software for performance memory and power Advanced guidelines for developing multicore software for embedded systems How to develop embedded software for networking storage and automotive segments How to manage the embedded development process Includes contributions from Frank Schirrmeister Shelly Gretlein Bruce Douglass Erich Styger Gary Stringham Jean Labrosse Jim Trudeau Mike Brogioli Mark Pitchford Catalin Dan Udma Markus Levy Pete Wilson Whit Waldo Inga Harris Xinxin Yang Srinivasa Addepalli Andrew McKay Mark Kraeling and Robert Oshana Road map of key problems issues and references to their solution in the text Review of core methods in the context of how to apply them Examples demonstrating timeless implementation details Short and to the point case studies show how key ideas can be implemented the rationale for choices made and design guidelines and trade offs

Software Engineering for Embedded Systems Gary Stringham, 2013-04-01 This chapter discusses the interface that hardware provides for the embedded software It discusses the registers and interrupts that provide that interface But there is more there are the human aspects of getting the hardware team and the embedded software team to collaborate on the project Collaboration is needed during the design phase the co development phase the

integration phase and the debugging phase and this chapter discusses those concepts Several hardware design aspects are discussed that improve the quality of the product and software design aspects are discussed to help support hardware versions

Collaborative Design for Embedded Systems John Fitzgerald,Peter Gorm Larsen,Marcel Verhoef,2014-04-23 One of the most significant challenges in the development of embedded and cyber physical systems is the gap between the disciplines of software and control engineering In a marketplace where rapid innovation is essential engineers from both disciplines need to be able to explore system designs collaboratively allocating responsibilities to software and physical elements and analyzing trade offs between them To this end this book presents a framework that allows the very different kinds of design models discrete event DE models of software and continuous time CT models of the physical environment to be analyzed and simulated jointly based on common scenarios The individual chapters provide introductions to both sides of this co simulation technology and give a step by step guide to the methodology for designing and analyzing co models They are grouped into three parts Part I introduces the technical basis for collaborative modeling and simulation with the Crescendo technology Part II continues with different methodological guidelines for creating co models and analyzing them in different ways using case studies Part III then delves into more advanced topics and looks into the potential future of this technology in the area of cyber physical systems Finally various appendices provide summaries of the VDM and 20 sim technologies a number of valuable design patterns applicable for co models and an acronym list along with indices and references to other literature By combining descriptions of the underlying theory with records of real engineers experience in using the framework on a series of case studies the book appeals to scientists and practitioners alike It is complemented by tools examples videos and other material on www.crescendotool.org Scientists researchers and graduate students working in embedded and cyber physical systems will learn the semantic foundations for collaborative modeling and simulation as well as the current capabilities and limitations of methods and tools in this field Practitioners will be able to develop an appreciation of the capabilities of the co modeling techniques to assess the benefits of more collaborative approaches to modeling and simulation and will benefit from the included guidelines and modeling patterns

Engineering Methods and Tools for Software Safety and Security M. Broy,W. Sitou,T. Hoare,2009-03-26 Based on the specific needs in applications of software technology models and formal methods must serve the needs and the quality of advanced software engineering methods This book provides a presentation of topics on how to meet such challenges covering both theoretical foundations and industrial practice

Design and Analysis of Distributed Embedded Systems Bernd Kleinjohann,K.H. (Kane) Kim,Lisa Kleinjohann,Achim Rettberg,2013-04-17 Design and Analysis of Distributed Embedded Systems is organized similar to the conference Chapters 1 and 2 deal with specification methods and their analysis while Chapter 6 concentrates on timing and performance analysis Chapter 3 describes approaches to system verification at different levels of abstraction Chapter 4 deals with fault tolerance and detection Middleware and software reuse aspects are treated in Chapter 5 Chapters

7 and 8 concentrate on the distribution related topics such as partitioning scheduling and communication The book closes with a chapter on design methods and frameworks

Computational Science and Its Applications - ICCSA 2006

Osvaldo Gervasi,Vipin Kumar,C.J. Kenneth Tan,David Taniar,Antonio Laganà,Youngsong Mun,Hyunseung Choo,2006-05-11

The five volume set LNCS 3980 3984 constitutes the refereed proceedings of the International Conference on Computational Science and Its Applications ICCSA 2006 The volumes present a total of 664 papers organized according to the five major conference themes computational methods algorithms and applications high performance technical computing and networks advanced and emerging applications geometric modelling graphics and visualization information systems and information technologies This is Part II

Fundamental Approaches to Software Engineering

Michel Wermelinger,Tiziana Margaria-Steffen,2004-03-01 ETAPS 2004 was the seventh instance of the European Joint Conferences on Theory and Practice of Software ETAPS is an annual federated conference that was established in 1998 by combining a number of existing and new conferences This year it comprised ve conferences FOSSACS FASE ESOP CC TACAS 23 satellite workshops 1 tutorial and 7 invited lectures not including those that are speci c to the satellite events The events that comprise ETAPS address various aspects of the system velopment process including speci cation design implementation analysis and improvement The languages methodologies and tools that support these act ities are all well within its scope Di erent blends of theory and practice are r resented with an inclination towards theory with a practical motivation on the one hand and soundly based practice on the other Many of the issues involved in software design apply to systems in general including hardware systems and theemphasis on software is not intended to be exclusive

Readings in Hardware/Software Co-Design

Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

Design Technology for

Heterogeneous Embedded Systems Gabriela Nicolescu,Ian O'Connor,Christian Piguat,2012-02-02 Design technology to address the new and vast problem of heterogeneous embedded systems design while remaining compatible with standard Moore flows i e capable of simultaneously handling both silicon complexity and system complexity represents one of the most important challenges facing the semiconductor industry today and will be for several years to come While the micro electronics industry over the years and with its spectacular and unique evolution has built its own specific design methods to focus mainly on the management of complexity through the establishment of abstraction levels the emergence of device heterogeneity requires new approaches enabling the satisfactory design of physically heterogeneous embedded systems for the widespread deployment of such systems Heterogeneous Embedded Systems compiled largely from a set of contributions from participants of past editions of the Winter School on Heterogeneous Embedded Systems Design Technology FETCH proposes a necessarily broad and holistic overview of design techniques used to tackle the various facets of heterogeneity in terms of technology and opportunities at the physical level signal representations and different abstraction levels

architectures and components based on hardware and software in all the main phases of design modeling validation with multiple models of computation synthesis and optimization It concentrates on the specific issues at the interfaces and is divided into two main parts The first part examines mainly theoretical issues and focuses on the modeling validation and design techniques themselves The second part illustrates the use of these methods in various design contexts at the forefront of new technology and architectural developments

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